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10596720 - GALL 2814

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Application Number		10596720
Filing Date		2006-06-22
First Named Inventor	Anderson, Samuel	
Art Unit	4126	
Examiner Name	Raj R. Gupta	
Attorney Docket Number	681443-1U1	

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	1	GAN K P, et al, "Poly flanked VDMOS (PFVDMOS): a superior technology for superjunction devices" 32nd Annual IEEE Power Electronics Specialists Conference. Conference Proceedings. Vancouver, Canada June 17-21, 2001, Annual Power Electronics Specialists Conference, New York, NY: IEEE, US vol. VOL 1 of 4. CCNF- 32, 17 June 2001 (2001-06-17) pages 2156-2159 XP010559256 ISBN: 0-7803-7067-8	<input type="checkbox"/>

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